

**In the Specification**

Please amend the paragraph that bridges pages 5 and 6 as follows:

· As further shown in FIG. 1, a first input signal path 12 and a second input signal path 14 together form the input differential signal pair 15. A first output signal path 16 and a second output signal path 18 form the output differential signal pair 19. The primary inductor 20 is connected between the first signal path 12 and the second signal path 14. Similarly, the secondary inductor 22 is connected between the first output signal path 16 and the second output signal path 18. A differential driver 3 generates the input differential signal pair 15 from a single line data signal 5. A differential receiver 7 converts the output differential signal pair ~~21~~ 19 into a single line data signal 9.